

Syllabus

PANJAB UNIVERSITY, CHANDIGARH

B.Sc. Part- III

SEMESTER-V (Paper-B)

Electronics and Solid State Devices-I

UNIT—I

Concepts of current and voltage sources, Thevenin's theorem, Norton's theorem, source conversion. CRO, Block diagram, construction and principle of working. Use of CRO for frequency, time period, special features of dual trace, phase measurements.

Energy band diagrams in semiconductors, direct and indirect semiconductors, Formula to calculate position of Fermi level in p and n semiconductors, Barrier formation, energy band diagram of $p-n$ junction. Formula for depletion width, qualitative ideas of current flow mechanism in forward and reverse biased diode. $v-i$ characteristics, static and dynamic resistance, depletion and diffusion capacitance, zener diode, LED, photodiode and solar cell.

UNIT—II

Diode circuits, clipping circuits, Rectification : half wave, full wave and bridge rectifiers, filter circuits (C, LC and π -filter), rectification efficiency and ripple factor in LC filter, voltage regulation circuit using zener diode, voltage multiplier circuits.

Bipolar junction transistor : Structure and working, different currents in transistor, switching action, characteristics of CB, CE and CC configuration, Active, cut off and saturation region.

Load line analysis of transistors, Q-point, transistor biasing and stabilization of operating point, fixed bias, collector to base bias, bias circuit with emitter resistor, voltage divider biasing circuit.

Working and analysis of CE amplifier using h -parameters, current, voltage and power gain, input and output impedance class A, B and C amplifiers.